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APPLICANTS Shunpei Yamazaki, Tokyo, JAPAN; Hisashi Ohtani, Kanagawa, JAPAN; Akiharu Miyanaaga, Kanagawa, JAPAN; Toru Mitsuki, Kanagawa, JAPAN; Takeshi Fukunaga, Kanagawa, JAPAN;				
** CONTINUING DATA ***** This application is a DIV of 09/115,094 07/13/1998 PAT 6,693,299				
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IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/12/2004				
Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature: [Signature] Initials: [Initials]	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 24/14
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TITLE Semiconductor device and method of manufacturing the same				
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